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THE UNITED STATES PATENT AND TRADEMARK OFFICE
(Case No. 211.001-D3-US)

In the Application of: **Fazan et al.**

Serial No: **10/724,648**

Filed: **December 1, 2003**

Title: **Semiconductor Device**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

) Group Art Unit: **2814**

) Examiner:

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on May 17, 2004

Date
Michiko Sato
(person signing this certificate)

Michiko Sato
Signature

STATEMENT OF RELATED APPLICATIONS

Dear Sir:

The above-referenced application is related to the following applications, all of which claim priority to "Semiconductor Device ", Serial No. 10/450,238, filed June 10, 2003:

1. "Semiconductor Device", Fazan et al., Serial No. 10/694,689, filed October 28, 2003 (still pending);
2. "Semiconductor Device", Fazan et al., Serial No. 10/724,377, filed November 28, 2003 (still pending);
3. "Semiconductor Device", Fazan et al., Serial No. 10/727,742, filed December 4, 2003 (still pending); and
4. "Semiconductor Device", Fazan et al., Serial No. 10/741,804, filed December 19, 2003 (still pending).

Respectfully submitted,

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650-968-8079

Date: May 17, 2004